

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Blaschke, et al.

Serial No.: Not Assigned

Filed: Herewith

**For: Composite Ground Shield for Passive
Components in a Semiconductor Die**

Art Unit: Not Assigned

Examiner: Not Assigned

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

In this Information Disclosure Statement and pursuant to 37 C.F.R. §§ 1.56 and 1.97, Applicants hereby disclose to the Patent Office patents, publications or other information of which Applicants are aware. Form 1449 and a copy of a technical paper are submitted herewith.

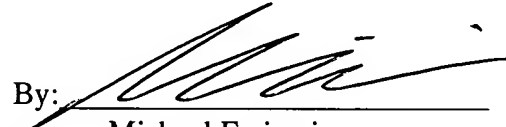
The items identified in this Information Disclosure Statement may or may not be “material” as defined in 37 C.F.R. § 1.56, and the submission thereof by Applicants is not to be construed as an admission that any such patent, publication or other information referred to is material or considered to be material (37 C.F.R. § 1.97(h)), or even qualifies as “prior art” under 35 U.S.C. § 102 with respect to the present invention unless specifically designated by Applicants as such. Identification of any reference or patent herein is not an admission, nor is it to be construed as an admission, that it was invented prior to the invention disclosed herein.

The filing of this Information Disclosure Statement is not to be construed to mean that a search has been made or that no other material information, as defined in 37 C.F.R. § 1.56, exists.

In accordance with 37 C.F.R. § 1.97 no fee is believed due at this time, since, in accordance with 37 C.F.R. § 1.97(b), this Information Disclosure Statement is being filed within three months of the filing date of the application as well as before the mailing of a first Office Action on the merits. The Assistant Commissioner is hereby authorized to charge or credit Deposit Account Number 50-0731 for any deficiency or overpayment in the fees required for the filing of this Information Disclosure Statement, for which purpose a duplicate copy of this paper is also included.

Respectfully submitted,
Farjami & Farjami LLP

Dated: 4/15/04

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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to addressee" Service under 37 C.F.R. Sec. 1.10 addressed to: Commissioner of Patents, P.O Box 1450, Alexandria, VA 22313-1450 on 4/16/04.

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FORM PTO-1449 LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 0150139	SERIAL NO. Not Assigned
	APPLICANTS: Blaschke, et al.	
	FILING DATE: Herewith	GROUP ART: Not Assigned

U.S. PATENT DOCUMENTS							
Exam. Initials		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	AA						
	AB						
	AC						

FOREIGN PATENT DOCUMENT								
Exam. Initials		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	BA							
	BB							
	BC							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
	CA	C. Patrick Yue and S. Simon Wong, "On-Chip Spiral Inductors with Patterned Ground Shields for Si-Based RF IC's" IEEE Journal of Solid-State Circuits, Vol. 33, No. 5, May 1998, pages 743-751.
	CB	Kihong Kim and Kenneth O., "Characteristics of an Integrated Spiral Inductor with an Underlying n-Well" IEEE Transactions on Electron Devices, Vol. 44, No. 9, September 1997, pages 1565-1567.

EXAMINER:	DATE CONSIDERED:
EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.	
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